EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|------|--|---|---------------------|---------|---------------------|
| L1 | 1 | "5838681".pn. and (bandwidth capacity bw memory queue buffer throughput queued queueing buffered buffering RAM SDRAM DRAM SRAM) | USPAT | OR | ON | 2008/03/14 12:09 |
| L2 | 738 | (switch (network adj1 processor) node) with (throughput capacity bandwidth bw) with (memory near3 (bw bandwidth)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:30 |
| L3 | 0 | (switch (network adj1 processor) node) with (throughput capacity bandwidth bw) with (memory near3 (bw bandwidth)) with definition | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:30 |
| L4 | 9 | (switch (network adj1 processor) node) with (throughput capacity bandwidth bw) with (memory near3 (bw bandwidth)) with (defining define defined) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:31 |
| L5 | 40 | (switch (network adj1 processor) node) with (throughput capacity bandwidth bw) with (memory near3 (bw bandwidth)) with (enough sufficient) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:37 |
| L6 | 29 | 5 and @ad<"20030918" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:37 |
| L7 | 11 | 5 not 6 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:55 |

| L8 | 0 | ((switch switching) near3 core) with (memory adj1 bandwidth) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:57 |
|-----|----|---|---|----|----|---------------------|
| L9 | 0 | ((switch switching) with core) with (memory adj1 bandwidth) | US PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:57 |
| L10 | 7 | ((switch switching) with core) same (memory adj1 bandwidth) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:57 |
| L11 | 32 | (((switch switching) with core) with (capacity throughput bandwidth bw)) same (memory near3 (bandwdth bw throughput capacity)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:58 |
| L12 | 0 | 10 and 11 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 12:59 |
| L13 | 19 | 11 and @ad<"20030918" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 13:02 |
| L14 | 13 | 11 not 13 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/14 13:11 |

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